

3.2 Circuits With Non-Zero Initial Conditions

3.2.1

DC & L 14.5

- Often, we deal with circuits that are not initially at rest — particularly when there are switches in the circuit (e.g. switched cap filters)
- ICs are straightforward to handle using LT.

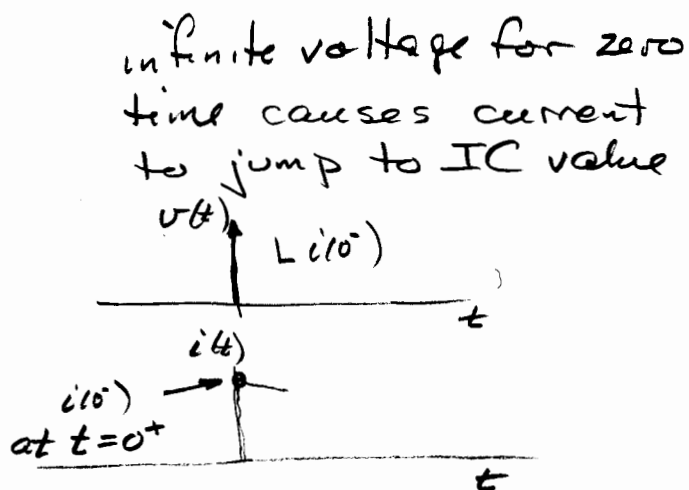
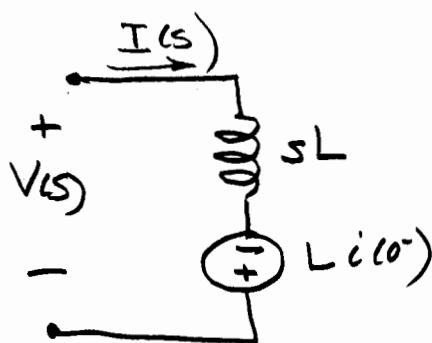
— inductor $v = L \frac{di}{dt}$

or

$$V(s) = L(s I(s) - i(0^-))$$

$$= sL I(s) - L i(0^-)$$

Second term is a constant, the LT of an impulse, and it adds an extra component of $V(s)$ — so add a fictitious voltage source (for a current IC)

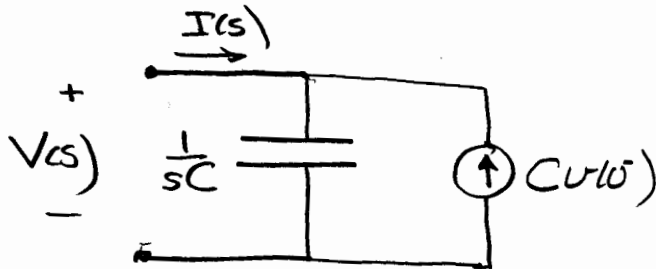


- capacitor $i = C \frac{dv}{dt}$

3.2.2

$$I(s) = C (s V(s) - v(0^-)) = sC V(s) - \underbrace{C v(0^-)}_{\text{transform of impulse}}$$

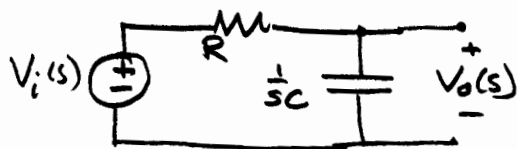
A fictitious current source to represent voltage IC



The impulse in current dumps an instant $C v(0^-)$ coulomb onto the cap, causing a voltage jump to $v(0^-)$

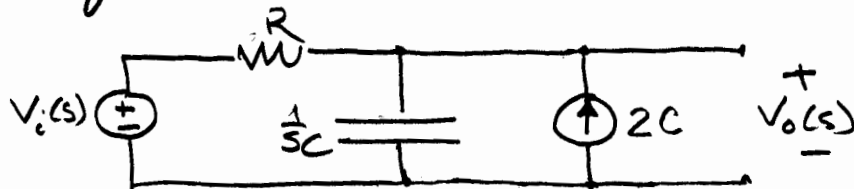
- and solve circuits using superposition, as if they were at rest

• example

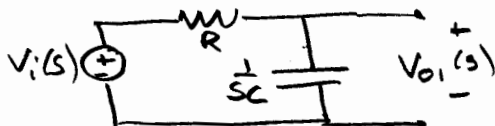


with $v_o(0^-) = 2V$

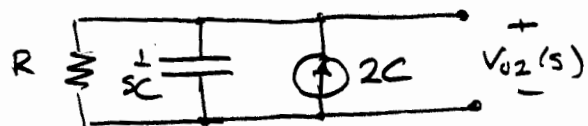
equivalent circuit:



superposition:



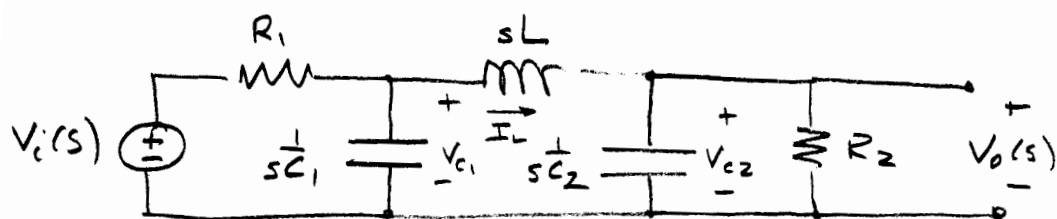
$$\begin{aligned} V_{o1}(s) &= \frac{1/sC}{R + 1/sC} V_i(s) \\ &= \frac{1}{1 + sRC} V_i(s) \end{aligned}$$



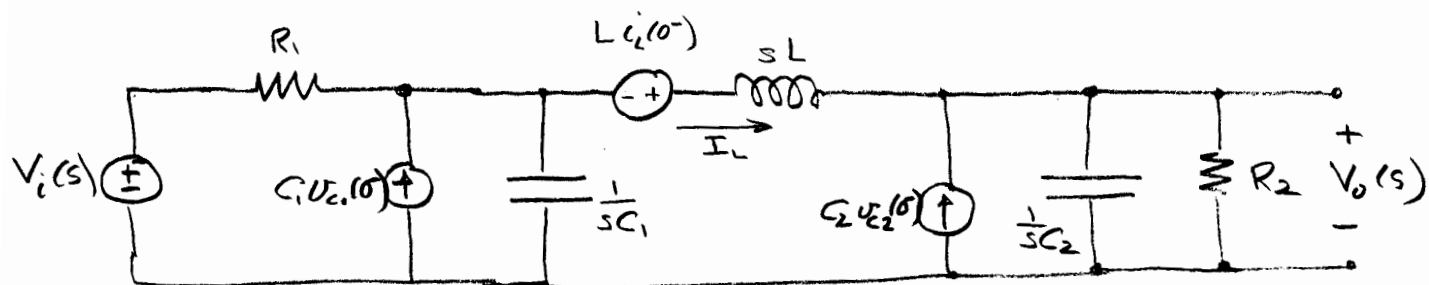
$$\begin{aligned} V_{o2}(s) &= \frac{R \cdot 1/sC}{R + 1/sC} \cdot 2C \\ &= \frac{2RC}{1 + sRC} \end{aligned}$$

what is $H(s)$ in this example?

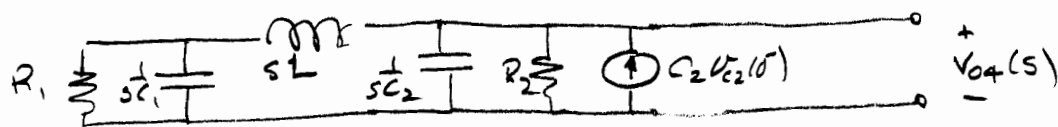
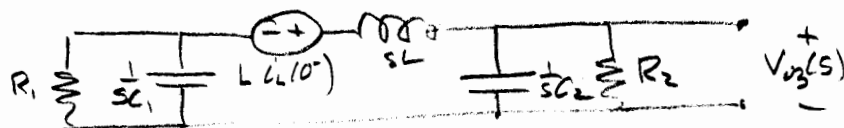
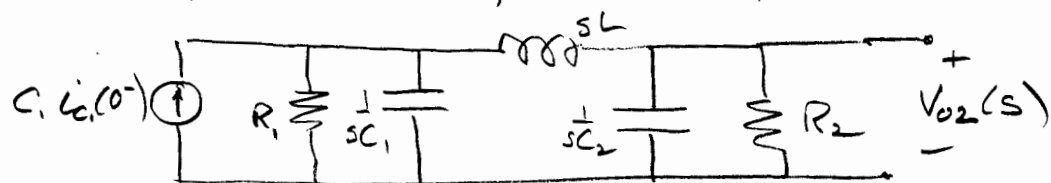
- A complicated example — a 3rd order lowpass



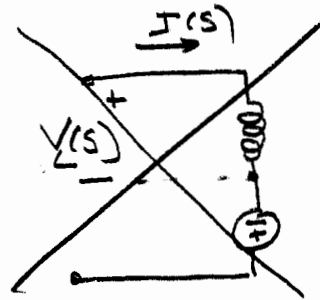
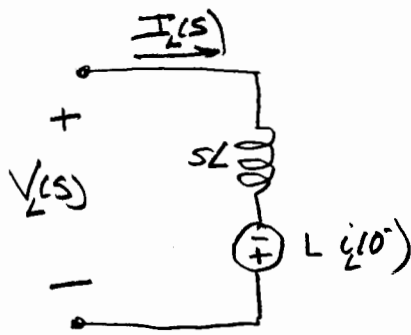
- If zero ICs, then solve by circuit reduction, or loop and/or node equations, all with impedances R , sL , $1/sC$.
- If non-zero IC's, redraw circuit



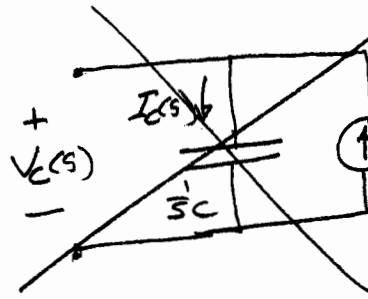
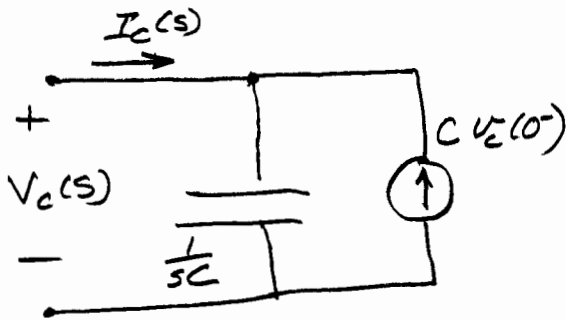
Total response is the superposition of responses to four sources; for example



- Be careful if the "output" is a circuit element with a non-zero IC

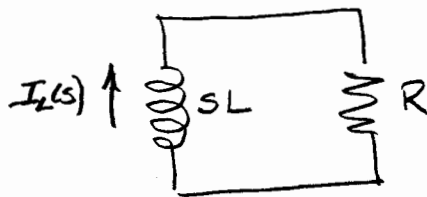


The inductor voltage $V_L(s)$ includes the IC source

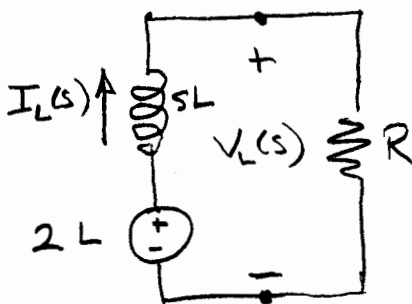


The cap current $I_C(s)$ must include the IC source

example



equiv cct



Initial inductor current is $i_L(0^-) = 2 \text{ A}$. Solve for inductor voltage $V_L(s)$.

① By voltage divider:

$$V_L(s) = \frac{R}{R + sL} \cdot 2L$$

② By current then voltage

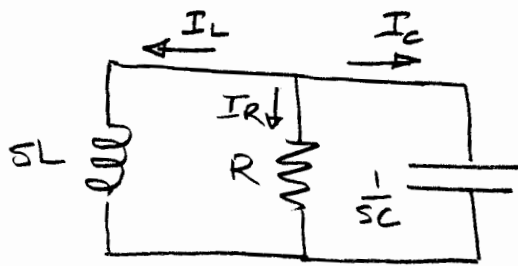
$$I_L(s) = \frac{2L}{sL + R}$$

$$\begin{aligned} \text{so } V_L(s) &= 2L - sL I_L(s) \\ &= 2L - \frac{2sL^2}{sL + R} \end{aligned}$$

$$= \frac{2RL}{sL + R}$$

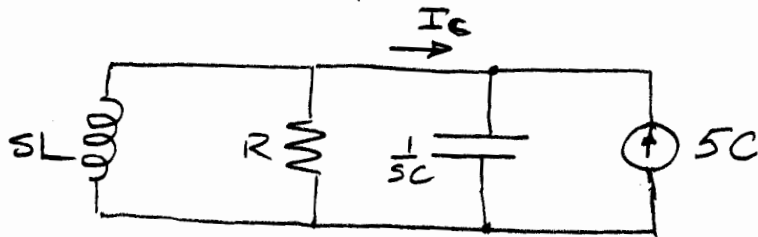
another example

3.2.5



+ The capacitor is initially
 V_C charged to 5 V and the
 - initial inductor current is 0.

Solve for capacitor current.



The cap current is not simply a current divider applied to $5C$; it must also include $5C$.

current divider alone:

$$5C \frac{sC}{sC + \frac{1}{R} + \frac{1}{sL}} = \frac{5C s^2}{s^2 + \frac{1}{RC}s + \frac{1}{LC}}$$

plus IC source:

$$I_C(s) = \frac{5C s^2}{s^2 + \frac{1}{RC}s + \frac{1}{LC}} - 5C \quad (\text{why "-"?})$$

$$= \frac{-\frac{5}{R}s - \frac{5}{L}}{s^2 + \frac{1}{RC}s + \frac{1}{LC}}$$

or $I_C(s) = -\text{current to } sL \parallel R = \frac{sLR}{sL+R}$, obtain by current div

$$= -5C \cdot \frac{\frac{1}{sC}}{\frac{sLR}{sL+R} + \frac{1}{sC}} = \frac{-5(\frac{s}{R} + \frac{1}{L})}{s^2 + \frac{1}{RC}s + \frac{1}{LC}}$$